

## REMARKS

Claim 16 is currently pending and was rejected under 35 U.S.C. § 102(e) as being anticipated by Wells and obvious from Reitmeier.

Claim 16 recites, among other limitations, "video decoder further comprising: "a decompression engine for decompressing compressed video data, there by resulting in decompressed video data; and a deinterlacer for deinterlacing the decompressed video data, thereby resulting in deinterlaced video data".

Examiner has indicated that "Wells discloses an integrated video decoder 202 (Figs. 3-8) which includes the decompression engine, which also includes a deinterlacer and scalar (para 0014, 0016), wherein the scalar is met by post processing stage 206.

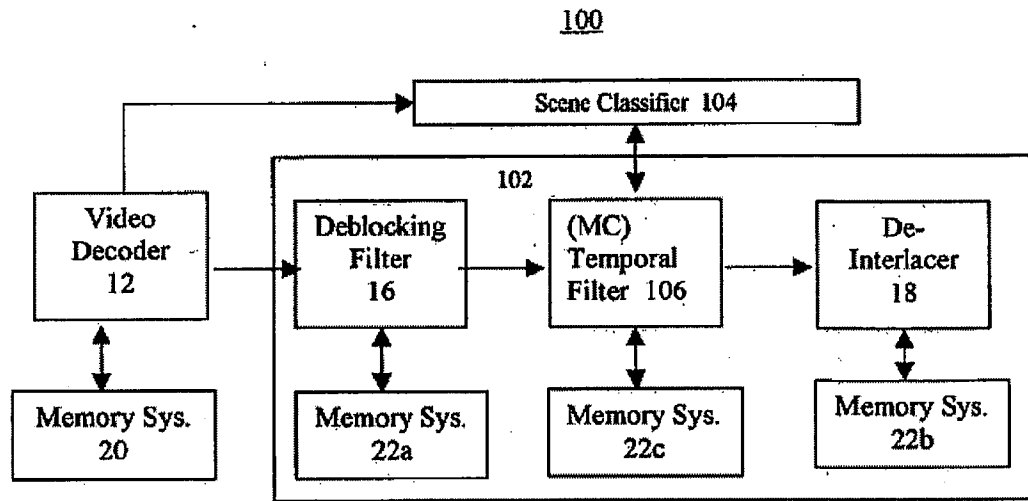
Wells, [0016] states:

**[0016]** The present invention provides an integrated video decoding system with spatial/temporal video post-processing. The integrated video decoding system includes a video decoder for decompressing an input data stream, and an integrated post-processing stage coupled to the video decoder. According to one aspect of the present invention, the integrated post-processing stage combines a temporal filter, a deinterlacer, and optionally a deblocking filter. The integrated post-processing stage further utilizes a single memory system used by the temporal filter and the deinterlacer.

It is noted that "The integrated video decoding system includes a video decoder ... and an integrated post-processing stage coupled to the video decoder. According to one aspect of the present invention, the integrated post-processing stage combines ... a deinterlacer." Note that it that the "post-processing stage coupled to the video

decoder", i.e., separate. It is the "post-processing stage" that combines the "deinterlacer". Thus, the foregoing does not teach "video decoder further comprising: ... a deinterlacer".

Moreover, Assignee calls Examiner's attention to Wells, Figure 3, reproduced below.



Wells, Figure 3.

As can be seen below, "Video Decoder 12" obviously does not comprise "De-interlacer 18".

Accordingly, for this reason alone, Wells does not teach or fairly suggest "video decoder further comprising: a decompression engine for decompressing compressed video data, thereby resulting in decompressed video data; and a deinterlacer for deinterlacing the decompressed video data, thereby resulting in deinterlaced video data". Thus, Assignee respectfully traverses the rejection and requests that Examiner withdraw it.

Claim 16 was also rejected as obvious from Reitmeir. Examiner has indicated that "Reitmeir does not disclose

that the video decoder includes the deinterlacer and display engine, though the decoder does include the claimed decompression engine." Rather, "As recently decided by the Supreme Court in KSR vs. Teleflex, if a person of ordinary skill in the art can implement a predictable variation, and would see the benefit of doing so, a [103] rejection likely bars its patentability. The premise of making integral, In re Larson, ... In re Wolfe, has been established [as] being obvious to one of ordinary skill in the art to integrate components, thus providing a predictable (i.e., expected) outcome/result." Office Action at 3.

Assignee respectfully traverses the rejection and notes that the claim language does not recite the use of the term "integral" or "to integrate". Thus, the present application is distinguishable from In re Larson and In re Wolfe.

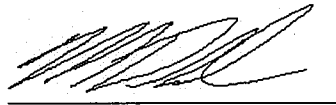
Accordingly, Assignee respectfully traverses the rejection to claim 16 and requests that Examiner withdraw the rejection.

Conclusion

For at least the foregoing reasons, each of the pending claims are allowable, thereby placing the application in a condition for allowance. A notice of allowance is respectfully requested.

The Commissioner is hereby authorized to charge any deficiency or credit any overpayment related to this action to Deposit Account No. 13-0017.

Respectfully Submitted,



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